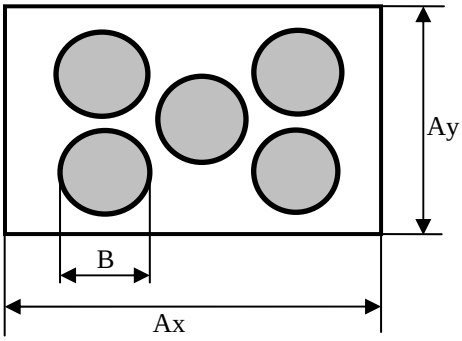
VSP-MIKRON

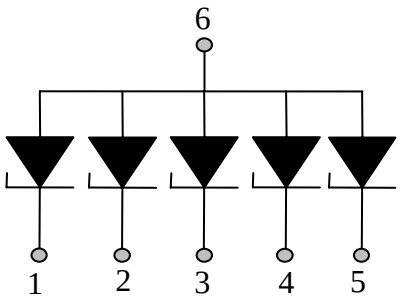
RoHS

Rev.1. March 2010.

KS-5,0V5

Chip low capacitance 5- fold ESD protection diode.





Mechanical date: A_x= 590um, A_y=450um
B=100um

Schematic and pinning diagram.

Chip thickness: 138+/-12 um
Scribe Line width - 60um.
Top Metal: Al - for wire bonding.
Back side - Anode: Ti-Ni-Ag for soldering.

Limiting values

Parameter	Symbol	Conditions	Value	Unit
Reverse Stand-off voltage	V_{RWM}	-	5,0	V
Peak Pulse Power	P_{pp}	t_p=8/20us	40*	W
Peak Pulse Current	I_{pp}	t_p=8/20us	3,5*	A
Electrostatic Discharge	V_{ESD}	IEC 61000-4-2, level 4.	>8 (Contact); >15 (Air).	kV
Max.junction temperature	T_j	-	+150	ºC

Characteristics (Tj=25ºC)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{BR}	Breakdown voltage	I_R=1mA	6,15	6,5	7,15	V
I_R	Reverse leakage current	V=5.0V	-	10	35	nA
V_{CL}	Clamping Voltage	I_{pp}=1.0A, t_p=8/20us I_{pp}=2.5A, t_p=8/20us I_{pp}=3.5A, t_p=8/20us	-	-	9* 11* 12*	V
C_J	Diode capacitance	V_R=0 V, f =1MHz	-	22	28	pF
R_{diff}	Differential resistance	I_R=1mA	-	20	100	Ohm

*- For Device testing