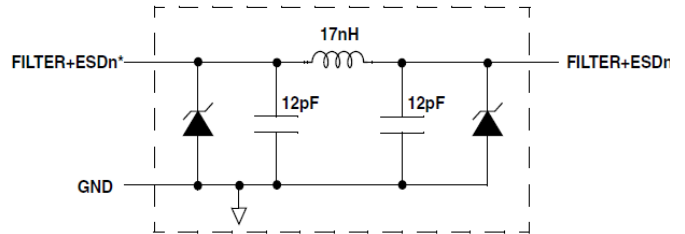
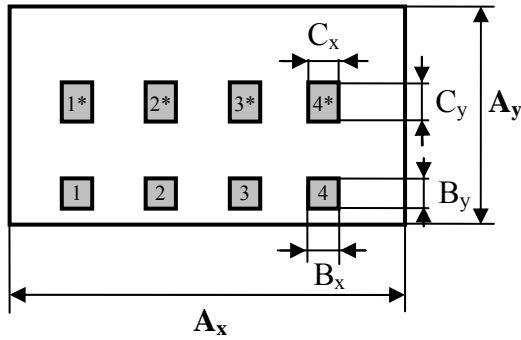




LC-304
PRELIMINARY

4 Channel L-C EMI Filter Array with ESD Protection.



Mechanical date: $A_x=1325\mu\text{m}$, $A_y=480\mu\text{m}$.
 $B_x=70\mu\text{m}$, $B_y=70\mu\text{m}$
 $C_x=82\mu\text{m}$, $C_y=106\mu\text{m}$

Schematic and pinning diagram.

Chip thickness: $138\pm 12\mu\text{m}$.
Scribe Line width - $60\mu\text{m}$.
Top Metal: Al – for wire bonding, $d=2.2\pm 0.2\mu\text{m}$.
Back side: Ti-Ni-Ag for soldering.
Back side – GND

Limiting values

Parameter	Symbol	Conditions	Value	Unit
Electrostatic Discharge	V_{ESD}	IEC 61000-4-2, level 4	± 15 (Contact)	kV
Max. junction temperature	T_j	—	125	$^{\circ}\text{C}$

Characteristics ($T_j=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
L	Channel Inductance	—	—	17	—	nH
C_{TOTAL}	Total Channel Capacitance	$V_R=2.5\text{V}$, 1MHz	18.8	23.5	28.2	pF
I_R	Diode reverse leakage current	$V=+3.3\text{V}$	—	0.1	1.0	μA
V_{SIG}	Signal Clamp Voltage Positive Clamp	$I_{LOAD}=10\text{mA}$	5.6	6.8	9	V
	Negative Clamp	$I_{LOAD}=-10\text{mA}$	-1.5	-0.8	-0.4	V