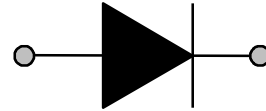
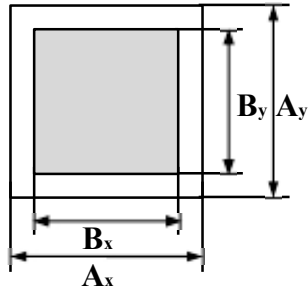


Rev.1. March 2012.

SM-05A

Chip TVS diode.



Mechanical date: $A_x=A_y=580\mu\text{m}$
 $B_x=B_y=420\mu\text{m}$

Chip thickness: $230\pm 20\mu\text{m}$

Scribe Line width - $60\mu\text{m}$.

Top Metal-Cathode: Al metallization for wire bond

Back side - Anode: Ti-Ni-Ag for soldering.

Schematic and pinning diagram.

Limiting values

Parameter	Symbol	Conditions	Value	Unit
Reverse Stand-off voltage	V_{RWM}	-	5,0	V
Peak Pulse Power	P_{pp}	$t_p=8/20\mu\text{s}$	350*	W
Peak Pulse Current	I_{pp}	$t_p=8/20\mu\text{s}$	24,0*	A
Electrostatic Discharge	V_{ESD}	IEC 61000-4-2, level 4.	>8(Contact); >15(Air).	kV
Max.junction temperature	T_j	-	+150	°C

Characteristics ($T_j=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{BR}	Breakdown voltage	$I_R=1\text{mA}$	6,0	6,8	-	V
I_R	Reverse leakage current	$V_R=5\text{V}$	-	-	5,0	μA
C_j	Diode capacitance .	$F=1\text{MHz}, V_{dc}=0\text{V}$.	-	-	350	pF
V_{CL}	Clamping voltage	$I_R= 5,0\text{A}, t_p=8/20\mu\text{s}$	-	-	9,8	V
		$I_R= 24,0\text{A}, t_p=8/20\mu\text{s}$	-	-	14,5	V

*- For Device testing